

SEMiX[®] 5

3-Level NPC IGBT-Module

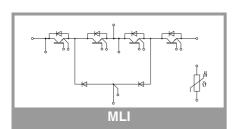
Engineering Sample SEMiX155MLI12E4

Target Data

Features

- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT 4 Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

- Case temperature limited to T_C=125°C max.
- Product reliability results are valid for $T_{jop}{=}150^{\circ}C$
- IGBT1: outer IGBTs T1 & T4
- IGBT2: inner IGBTs T2 & T3
- Diode1: outer diodes D1 & D4
 Diode0: inner diodes D0 & D2
- Diode2: inner diodes D2 & D3
- Diode5: clamping diodes D5 & D6
 For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"



Absolute	e Maximum Ratin	gs		
Symbol	Conditions		Values	Unit
IGBT1				
V _{CES}	T _j = 25 °C		1200	V
		T _c = 25 °C	237	A
.0	Τ _j = 175 °C	$T_c = 80 ^{\circ}C$	182	A
I _{Cnom}			150	A
	I _{CRM} = 3 x I _{Cnom}		450	Α
V _{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}, V_{GE} \le 15 \text{ V}, T_j = 150 \text{ °C}, V_{CES} \le 1200 \text{ V}$		10	μs
Tj			-40 175	°C
IGBT2				•
V _{CES}	T _j = 25 °C		1200	V
I _C	T 475.00	T _c = 25 °C	211	Α
	− T _j = 175 °C	T _c = 80 °C	162	Α
I _{Cnom}			150	Α
I _{CRM}	I _{CRM} = 3 x I _{Cnom}		450	Α
V _{GES}			-20 20	V
t _{psc}	V _{CC} = 800 V, V _{GE} V _{CES} ≤ 1200 V	\leq 15 V, T _j = 150 °C,	10	μs
Tj			-40 175	°C
Diode1	•	·		
V _{RRM}	T _j = 25 °C		1200	V
l _F		T _c = 25 °C	191	Α
	Τ _j = 175 °C	T _c = 80 °C	143	Α
I _{Fnom}			150	Α
I _{FRM}	I _{FRM} = 2 x I _{Fnom}		300	Α
I _{FSM}	10 ms, sin 180°,	T _i = 25 °C	774	Α
Tj		-	-40 175	°C
Diode2	I			
V _{RRM}	T _j = 25 °C		1200	V
I _F		T _c = 25 °C	177	А
· ·	− T _j = 175 °C	$T_c = 80 \degree C$	132	Α
I _{Fnom}			150	Α
I _{FRM}	I _{FRM} = 2 x I _{Fnom}		300	А
I _{FSM}	10 ms, sin 180°,	T _i = 25 °C	774	Α
Tj			-40 175	°C
Diode5				I
V _{RRM}	T _j = 25 °C		1200	V
IF		T _c = 25 °C	183	А
	T _j = 175 °C	$T_c = 80 \ ^{\circ}C$	137	А
I _{Fnom}			150	A
I _{FRM}	I _{FRM} = 2 x I _{Fnom}		300	A
I _{FSM}	$10 \text{ ms}, \sin 180^\circ, T_i = 25 \text{ °C}$		774	Α
Tj			-40 175	°C
Module	<u> </u>			I
I _{t(RMS)}			240	А
	module without T	IM	-40 125	°C
				V
T _{stg} V _{isol}	Module without T		-40 125 4000	



SEMiX[®] 5

3-Level NPC IGBT-Module

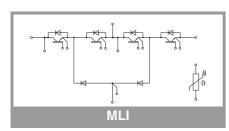
Engineering Sample SEMiX155MLI12E4

Target Data

Features

- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT 4 Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532NTC temperature sensor inside

- Case temperature limited to T_C=125°C max.
- Product reliability results are valid for $T_{jop}{=}150^{\circ}C$
- IGBT1: outer IGBTs T1 & T4
- IGBT2: inner IGBTs T2 & T3
- Diode1: outer diodes D1 & D4
- Diode2: inner diodes D2 & D3
- Diode5: clamping diodes D5 & D6
 For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"



Characte	1		l .			I
Symbol	Conditions		min.	typ.	max.	Uni
IGBT1						_
V _{CE(sat)}	$I_{\rm C} = 150 \rm{A}$	T _j = 25 °C		1.85	2.10	V
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.25	2.45	v
V _{CE0}		T _i = 25 °C		0.80	0.90	V
	- chiplevel	T _i = 150 °C		0.70	0.80	V
r _{CE}	V _{GE} = 15 V	T _i = 25 °C		7.0	8.0	mΩ
	chiplevel	T _i = 150 °C		10	11	mΩ
V _{GE(th)}	$V_{GE} = V_{CE}, I_C = 6 m$,	5	5.8	6.5	V
ICES	$V_{GE} = 0 V, V_{CE} = 12$				2	mA
Cies		f = 1 MHz		8.8		nF
Coes	$V_{CE} = 25 V$	f = 1 MHz		0.58		nF
Cres	V _{GE} = 0 V	f = 1 MHz		0.47		nF
Q _G	V _{GE} = - 8 V+ 15 V			850		nC
R _{Gint}	T _i = 25 °C			5.0		Ω
t _{d(on)}	$V_{CC} = 600 V$	T _i = 150 °C		61		ns
t _r	l _C = 150 A	$T_i = 150 ^{\circ}C$		53		ns
E _{on}	V _{GE} = +15/-8 V	$T_i = 150 ^{\circ}C$		12		m
t _{d(off)}	$R_{G \text{ on}} = 0.5 \Omega$ $R_{G \text{ off}} = 1 \Omega$	$T_i = 150 ^{\circ}C$		430		ns
t _f	$h_{G off} = 1.22$ di/dt _{on} = 3000 A/µs	,		115		ns
ч	$di/dt_{off} = 1200 \text{ A/}\mu\text{s}$			110		
E _{off}	du/dt = 3730 V/µs	T _j = 150 °C		17.3		mJ
R _{th(j-c)}	per IGBT				0.18	K/V
R _{th(c-s)}	per IGBT (λgrease=	=0.81 W/(m*K))		0.056		K/V
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.044		K/V
IGBT2						
V _{CE(sat)}	I _C = 150 A	T _i = 25 °C		1.85	2.10	V
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.25	2.45	V
V _{CE0}	chiployel	T _j = 25 °C		0.80	0.90	V
	- chiplevel	T _j = 150 °C		0.70	0.80	V
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		7.0	8.0	mΩ
	chiplevel	T _j = 150 °C		10	11	mΩ
V _{GE(th)}	$V_{GE} = V_{CE}, I_C = 6 m$	A	5	5.8	6.5	V
ICES	V _{GE} = 0 V, V _{CE} = 12	00 V, T _i = 25 °C			2	mA
Cies		f = 1 MHz		8.8		nF
C _{oes}	$V_{CE} = 25 V$	f = 1 MHz		0.58		nF
C _{res}	$V_{GE} = 0 V$	f = 1 MHz		0.47		nF
Q _G	V _{GE} = - 8 V+ 15 V	I		850		nC
R _{Gint}	T _i = 25 °C			5.0		Ω
t _{d(on)}	V _{CC} = 600 V	T _i = 150 °C		85		ns
t _r	$I_{\rm C} = 150 \rm{A}$	T _i = 150 °C		47		ns
E _{on}	$V_{GE} = +15/-8$ V	T _i = 150 °C		7.6		mJ
t _{d(off)}	$R_{G on} = 0.5 \Omega$ $R_{G off} = 1 \Omega$	T _i = 150 °C		445		ns
t _f	$di/dt_{on} = 2860 \text{ A}/\mu \text{s}$,		105		ns
E _{off}	di/dt _{off} = 1140 A/μs du/dt = 3680 V/μs			18.3		mJ
R _{th(j-c)}	per IGBT				0.22	K/V
R _{th(c-s)}	per IGBT (λgrease	=0.81 W/(m*K))		0.07		K/V
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.056		K/V



SEMiX[®] 5

3-Level NPC IGBT-Module

Engineering Sample SEMiX155MLI12E4

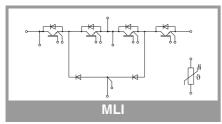
Target Data

Features

- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT 4 Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

- Case temperature limited to T_C=125°C max.
- Product reliability results are valid for $T_{jop}{=}150^{\circ}C$
- IGBT1: outer IGBTs T1 & T4
- IGBT2: inner IGBTs T2 & T3
- Diode1: outer diodes D1 & D4
- Diode2: inner diodes D2 & D3
- Diode5: clamping diodes D5 & D6
 For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"

						1
Symbol	Conditions		min.	typ.	max.	Ur
Diode1						
$V_F = V_{EC}$	$I_{\rm F} = 150 \rm{A}$	T _j = 25 °C		2.17	2.49	\
	V _{GE} = 0 V chiplevel	T _j = 150 °C		2.11	2.42	١
V _{F0}		T _i = 25 °C		1.30	1.50	
10	- chiplevel	, Т _i = 150 °С		0.90	1.10	1
r _F		T _i = 25 °C		5.8	6.6	m
· · · · · · · · · · · · · · · · · · ·	- chiplevel	T _i = 150 °C		8.1	8.8	m
I _{RRM}	I _F = 150 A	T _i = 150 °C		129		ļ
Q _{rr}	$di/dt_{off} = 2860 \text{ A/}\mu\text{s}$	T _i = 150 °C		21		μ
E _{rr}	V _{CC} = 600 V V _{GE} = +15/-8 V	T _j = 150 °C		5.5		n.
R _{th(j-c)}	per diode				0.3	K/
R _{th(c-s)}	per diode (λgrease	=0.81 W/(m*K))		0.065	0.0	K/
_	per diode (Agrease				-	
R _{th(c-s)}	material	ieu prizee enange		0.06		K/
Diode2						
$V_F = V_{EC}$	$I_{\rm F} = 150 {\rm A}$	T _j = 25 °C		2.17	2.49	١
	V _{GE} = 0 V chiplevel	T _j = 150 °C		2.11	2.42	N
V _{F0}	chiplevel	T _i = 25 °C		1.30	1.50	١
10		T _i = 150 °C		0.90	1.10	١
r _F		T _i = 25 °C		5.8	6.6	m
· · · · · · · · · · · · · · · · · · ·	- chiplevel	T _i = 150 °C		8.1	8.8	m
I _{RRM}	I _F = 150 A	T _i = 150 °C		129		
Q _{rr}		T _i = 150 °C		21		μ
E _{rr}	V _R = 600 V V _{GE} = +15/-8 V	T _j = 150 °C		-		m
R _{th(j-c)}	per diode				0.34	K/
R _{th(c-s)}	per diode (λgrease	=0.81 W/(m*K))		0.075		Κ/
R _{th(c-s)}	per diode, pre-appl material	ied phase change		0.065		K/
Diode5						
$V_F = V_{EC}$	I _F = 150 A	T _i = 25 °C		2.17	2.49	1
1 20		T _i = 150 °C		2.11	2.42	\
	chiplevel	-				
V _{F0}	chiplevel	$T_j = 25 \degree C$		1.30	1.50	
~		$T_j = 150 \ ^{\circ}C$		0.90	1.10	\
r _F	chiplevel	$T_j = 25 \degree C$		5.8	6.6	m
1	I _F = 150 A	T _j = 150 °C		8.1	8.8	m
	$di/dt_{off} = 3000 \text{ A/}\mu\text{s}$	T _j = 150 °C T _i = 150 °C		153		/
Q _{rr}	V _R = 600 V	1j=150 C		25		μ
E _{rr}	V _{GE} = +15/-8 V	T _j = 150 °C		13.2		n
R _{th(j-c)}	per diode				0.32	K/
R _{th(c-s)}	per diode (λgrease			0.087		K/
R _{th(c-s)}	per diode, pre-appl material	ied phase change		0.079		K/





SEMiX[®] 5

3-Level NPC IGBT-Module

Engineering Sample SEMiX155MLI12E4

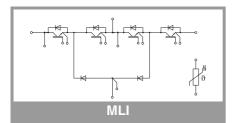
Target Data

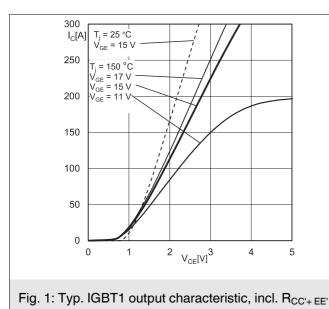
Features

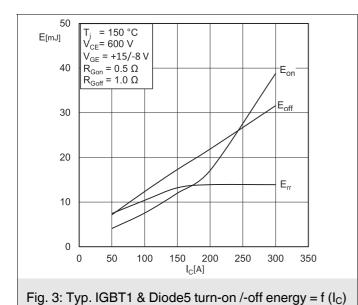
- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT 4 Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

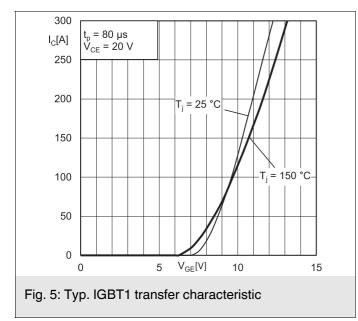
- Case temperature limited to T_C=125°C max.
- Product reliability results are valid for $T_{jop}{=}150^{\circ}C$
- IGBT1: outer IGBTs T1 & T4
- IGBT2: inner IGBTs T2 & T3
- Diode1: outer diodes D1 & D4
- Diode2: inner diodes D2 & D3
- Diode5: clamping diodes D5 & D6
- For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"

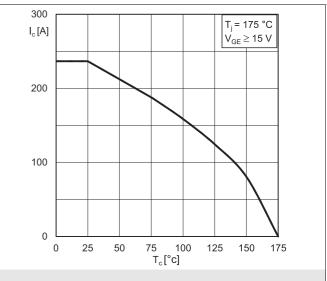
Characte	ristics					
Symbol	Conditions		min.	typ.	max.	Unit
Module						
L _{sCE1}				27		nH
L _{sCE2}				34		nH
R _{CC'+EE'}	measured between terminal 5 and 1	T _C = 25 °C	0.8			mΩ
		T _C = 125 °C	1.1			mΩ
R _{th(c-s)1}	calculated without t	hermal coupling		0.007		K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module (λ_{grease} =0.81 W/ (m*K))		0.0112			K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.0095		K/W
Ms	to heat sink (M5)		3		6	Nm
Mt		to terminals (M6)	3		6	Nm
						Nm
w				398		g
Temperat	ure Sensor					1
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)			493 ± 5%		Ω
B _{100/125}	R _(T) =R ₁₀₀ exp[B _{100/125} (1/T-1/T ₁₀₀)]; T[K];		3550 ±2%			к

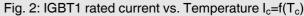


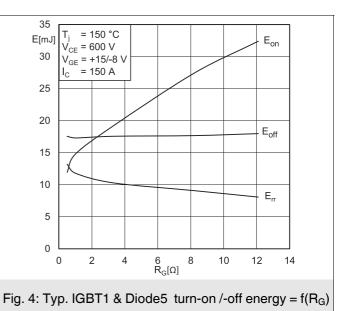


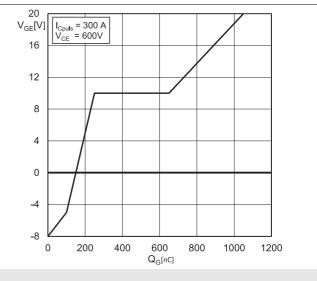


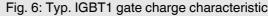


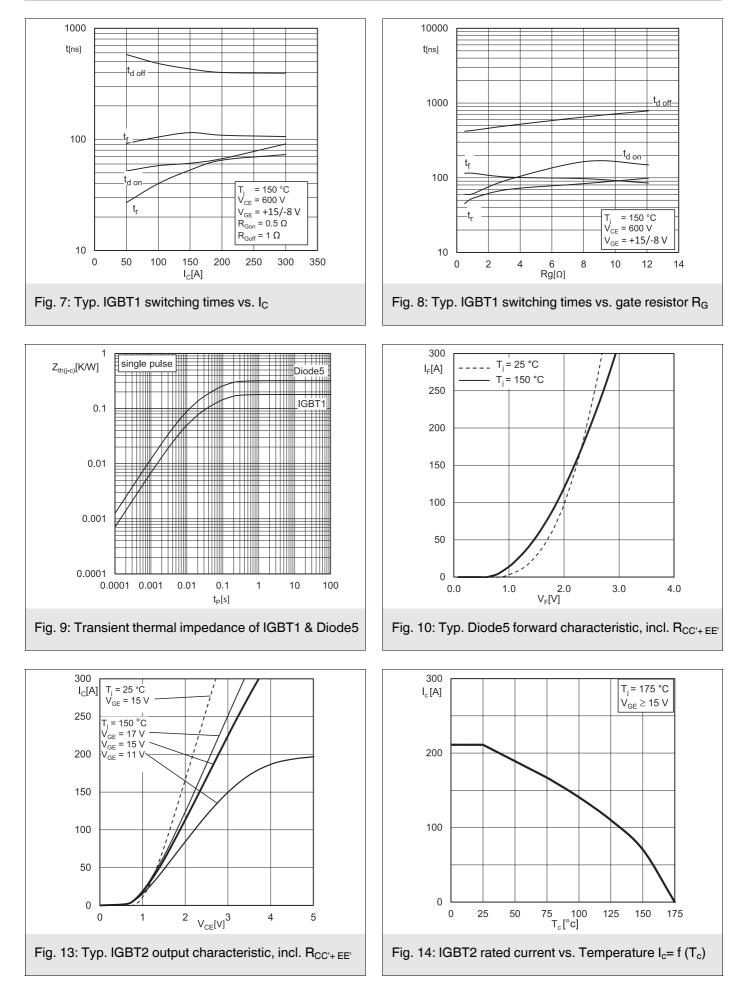






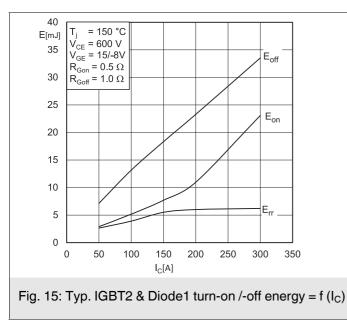


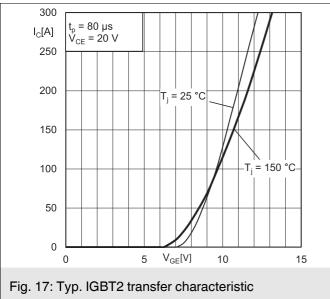


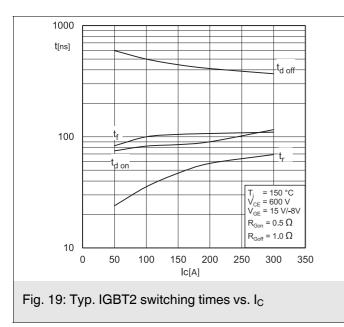


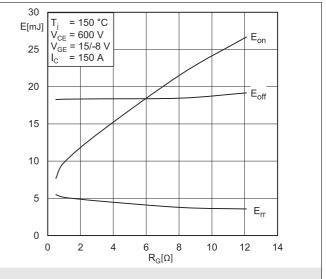
Rev. 0.9 - 10.01.2018

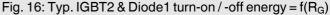
6











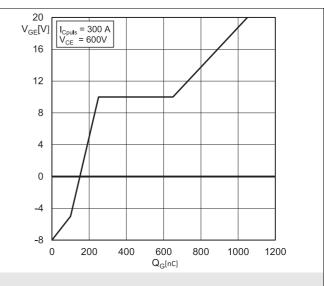


Fig. 18: Typ. IGBT2 gate charge characteristic

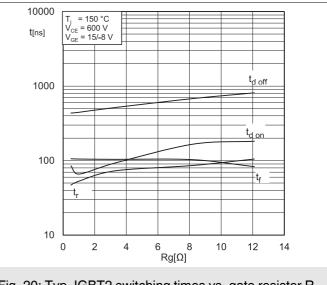
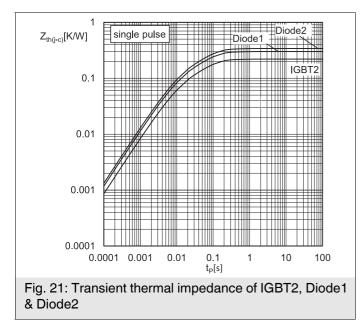
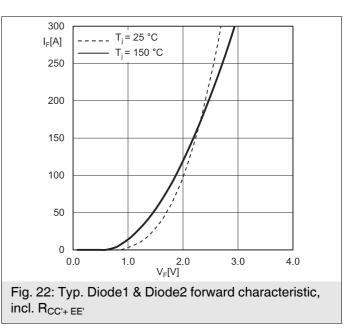
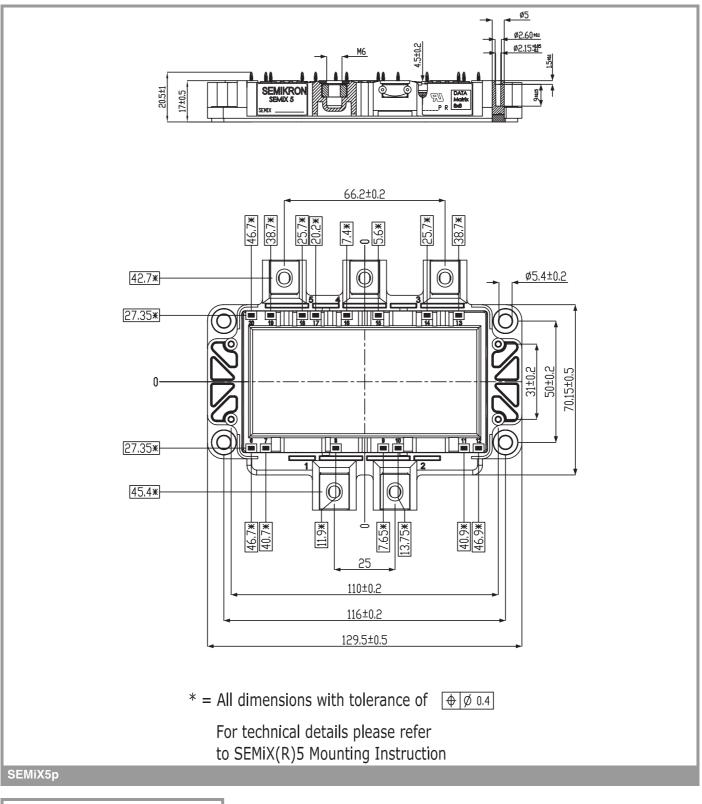
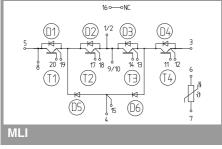


Fig. 20: Typ. IGBT2 switching times vs. gate resistor R_G









This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

***IMPORTANT INFORMATION AND WARNINGS**

The specifications of SEMIKRON products may not be considered as guarantee or assurance of product characteristics ("Beschaffenheitsgarantie"). The specifications of SEMIKRON products describe only the usual characteristics of products to be expected in typical applications, which may still vary depending on the specific application. Therefore, products must be tested for the respective application in advance. Application adjustments may be necessary. The user of SEMIKRON products is responsible for the safety of their applications embedding SEMIKRON products and must take adequate safety measures to prevent the applications from causing a physical injury, fire or other problem if any of SEMIKRON products become faulty. The user is responsible to make sure that the application design is compliant with all applicable laws, regulations, norms and standards. Except as otherwise explicitly approved by SEMIKRON in a written document signed by authorized representatives of SEMIKRON, SEMIKRON products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury. No representation or warranty is given and no liability is assumed with respect to the accuracy, completeness and/or use of any information herein, including without limitation, warranties of non-infringement of intellectual property rights of any third party. SEMIKRON does not assume any liability arising out of the applications or use of any product; neither does it convey any license under its patent rights, copyrights, trade secrets or other intellectual property rights, nor the rights of others. SEMIKRON makes no representation or warranty of non-infringement or alleged non-infringement of intellectual property rights of any third party which may arise from applications. Due to technical requirements our products may contain dangerous substances. For information on the types in question please contact the nearest SEMIKRON sales office. This document supersedes and replaces all information previously supplied and may be superseded by updates. SEMIKRON reserves the right to make changes.

In accordance with the quality guidelines of SEMIKRON, we would like to point out that the products are engineering samples. These engineering samples are not yet produced under quality conditions approaching those of series production, and are at the present time not included in the SEMIKRON quality monitoring and control process. Neither the product nor the production process has to date gone completely through the SEMIKRON internal authorization procedure. SEMIKRON may make any amendments without any prior notification. SEMIKRON cannot and shall not promise or commit itself to release and/or make available a final version or series product after the development phase. SEMIKRON cannot and will not assume any responsibility with regard to freedom from defects, functionality, and adaptation to and interaction with possible applications of the user or with regard to any other potential risks resulting from the use of engineering samples. Therefore SEMIKRON from any and all risks, damages, losses, expenses and costs directly or indirectly resulting out of or in connection with the commissioning, operation, system integration, sale, dissemination or any other kind of use of engineering samples by the customer and/or any third party, which has come into possession of engineering samples through or because of the customer. All know-how and all registerable and non-registerable copyrights and industrial property rights arising from or in connection with these engineering samples remain the exclusive property of SEMIKRON.