

SEMITRANS[®] 3

IGBT4 Modules

SKM300GB17E4H16

Features

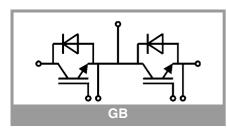
- H16: IGBT-chip with improved robustness against moisture
- IGBT4 = 4. generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4. Generation CAL-Diode
- Insulated copper baseplate using DBC Technology (Direct Copper Bonding)
- With integrated Gate resistor
- For switching frequencies up to 8kHzUL recognized, file no. E63532

Typical Applications*

• Medium voltage inverter market

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for T_i = 150°C



Absolute	Maximum Ratir	ngs			
Symbol	Conditions		Values	Unit	
IGBT					
V _{CES}	T _j = 25 °C		1700	V	
lc	T _j = 175 °C	T _c = 25 °C	500	А	
		T _c = 80 °C	380	Α	
I _{Cnom}			300	A	
I _{CRM}	$I_{CRM} = 3 x I_{Cnom}$		900	Α	
V _{GES}			-20 20	V	
t _{psc}	$V_{CC} = 1000 V$ $V_{GE} \le 15 V$ $V_{CES} \le 1700 V$	T _j = 150 °C	10	μs	
Tj			-40 175	°C	
Inverse d	iode			ľ	
V _{RRM}	T _j = 25 °C		1700	V	
l _F	T _j = 175 °C	T _c = 25 °C	314	А	
		T _c = 80 °C	231	Α	
I _{Fnom}			300	A	
I _{FRM}	$I_{FRM} = 2xI_{Fnom}$		600	Α	
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		1836	А	
Tj			-40 175	°C	
Module	•			•	
I _{t(RMS)}			500	А	
T _{stg}			-40 125	°C	
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	V	

Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
V _{CE(sat)}	I _C = 300 A	T _j = 25 °C		1.97	2.28	V
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.29	2.54	V
V _{CE0}	chiplevel	T _j = 25 °C		1.10	1.20	V
		T _j = 150 °C		1.00	1.10	V
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		2.9	3.6	mΩ
		T _j = 150 °C		4.3	4.8	mΩ
V _{GE(th)}	$V_{GE} = V_{CE}, I_C = 11.2 \text{ mA}$		5.2	5.8	6.5	V
I _{CES}	$V_{GE} = 0 V, V_{CE} = 17$	00 V, T _j = 25 °C			4.0	mA
Cies	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		22.0		nF
Coes		f = 1 MHz		0.92		nF
C _{res}		f = 1 MHz		0.72		nF
Q _G	V _{GE} = - 8 V+ 15 V			2400		nC
R _{Gint}	T _j = 25 °C			4.0		Ω
t _{d(on)}	$V_{GE} = +15/-15 V$ $R_{G on} = 1 \Omega$	T _j = 150 °C		325		ns
t _r		T _j = 150 °C		45		ns
Eon		T _j = 150 °C		106		mJ
t _{d(off)}		T _j = 150 °C		750		ns
t _f		T _j = 150 °C		165		ns
E _{off}		T _j = 150 °C		122		mJ
R _{th(j-c)}	per IGBT	1			0.083	K/W
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.043		K/W



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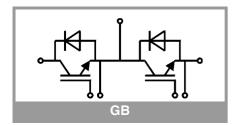
Typical Applications*

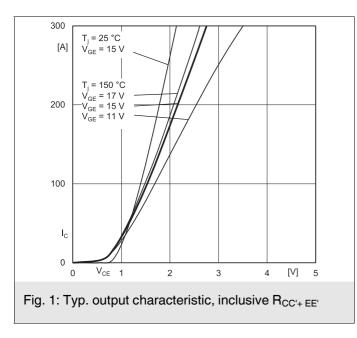
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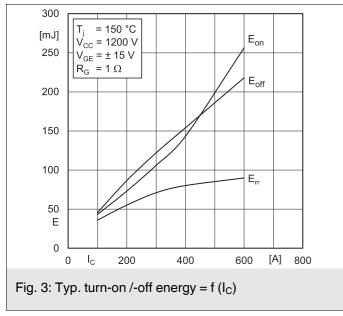
Remarks

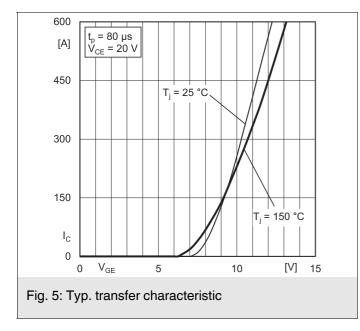
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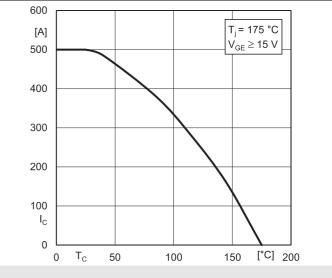
Characte	ristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse d	iode					
		T _j = 25 °C	1	2.00	2.40	V
		T _j = 150 °C		2.14	2.56	V
V _{F0}	chiplevel	T _j = 25 °C		1.32	1.56	V
		T _j = 150 °C		1.08	1.22	V
r _F	chiplevel	T _j = 25 °C		2.3	2.8	mΩ
		T _j = 150 °C		3.5	4.5	mΩ
I _{RRM}	$I_{F} = 300 \text{ A} \\ di/dt_{off} = 7100 \text{ A/}\mu\text{s} \\ V_{GE} = \pm 15 \text{ V} \\ V_{CC} = 1200 \text{ V} $	T _j = 150 °C		450		Α
Q _{rr}		T _j = 150 °C		102		μC
E _{rr}		T _j = 150 °C		71		mJ
R _{th(j-c)}	per diode				0.19	K/W
R _{th(c-s)}	per diode (λ_{grease} =0.81 W/(m*K))			0.051		K/W
Module	·					
L _{CE}				15		nH
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.55		mΩ
		T _C = 125 °C		0.85		mΩ
R _{th(c-s)1}	calculated without thermal coupling (λ _{grease} =0.81 W/(m*K))			0.012		K/W
R _{th(c-s)2}	including thermal co Ts underneath mod $(\lambda_{grease}=0.81 \text{ W/(m}^*)$		0.019		K/W	
Ms	to heat sink M6		3		5	Nm
Mt		to terminals M6	2.5		5	Nm
	-1					Nm
w	1	1	1		325	g

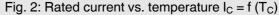


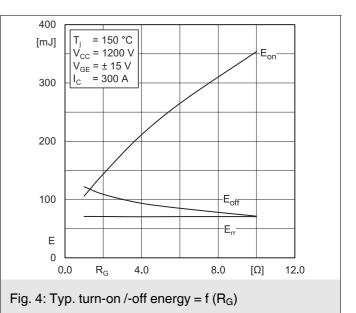


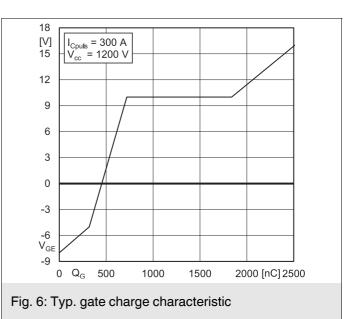


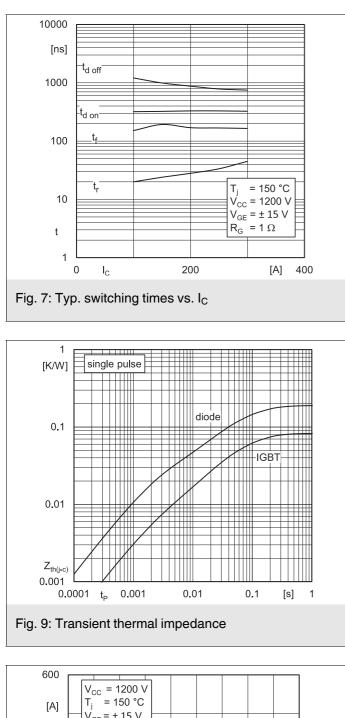


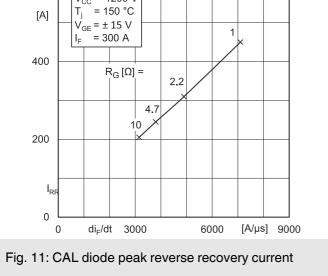












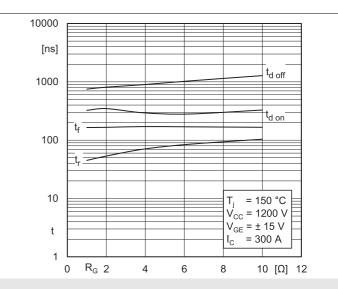
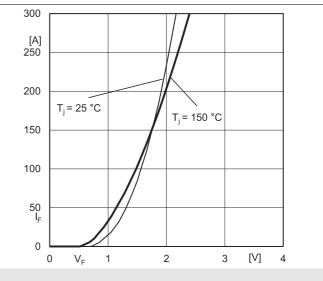
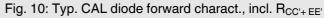
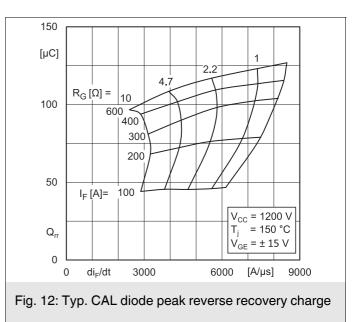
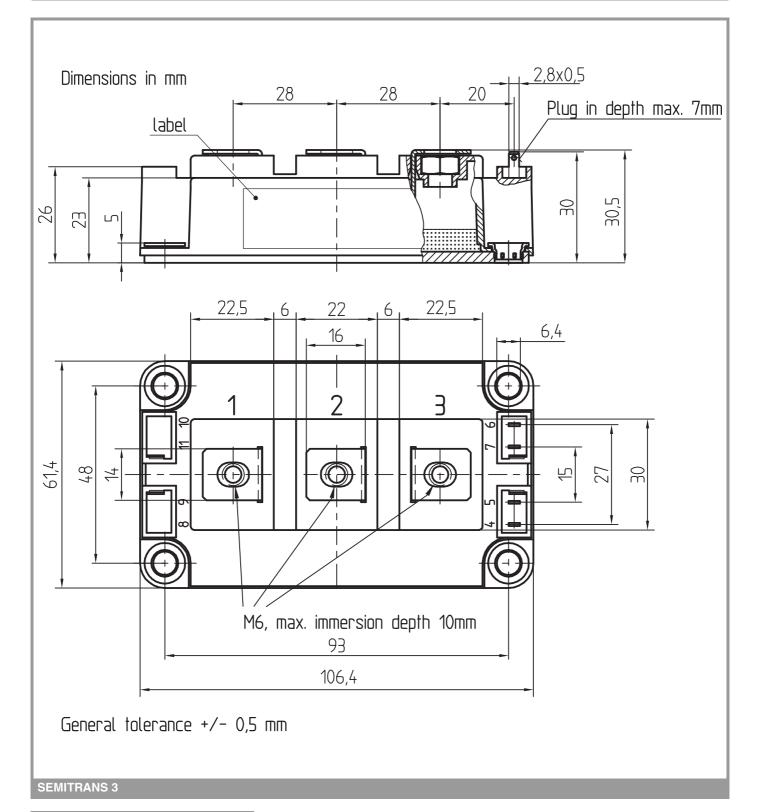


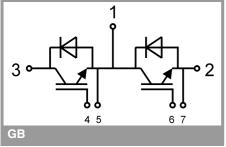
Fig. 8: Typ. switching times vs. gate resistor R_G











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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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